

# Fully Abstract Encodings of $\lambda$ -Calculus in HOcore through Abstract Machines

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**Abstract**—We present fully abstract encodings of the call-by-name  $\lambda$ -calculus into HOcore, a minimal higher-order process calculus with no name restriction. We consider several equivalences on the  $\lambda$ -calculus side—normal-form bisimilarity, applicative bisimilarity, and contextual equivalence—that we internalize into abstract machines in order to prove full abstraction.

## I. INTRODUCTION

HOcore is a minimal process calculus with higher-order communication, meaning that messages are executable processes. It is a subcalculus of HO $\pi$  [19] with no construct to generate names or to restrict the scope of communication channels. Even with such a limited syntax, HOcore is Turing complete [16]. However, as a higher-order calculus, it is less expressive than the name passing  $\pi$ -calculus: polyadic message sending cannot be compositionally encoded in monadic message sending in HO $\pi$  [15], while it can be done in  $\pi$  [21].

Although HOcore is Turing complete, we initially thought a fully abstract encoding of the  $\lambda$ -calculus into HOcore was impossible. Indeed, a  $\lambda$ -term potentially has an unbounded number of redexes. A straightforward encoding would use communication to emulate  $\beta$ -reduction, but since HOcore does not provide means to restrict the scope of communication, one would need as many distinct names as there are redexes to avoid interference. Moreover, as new redexes may be created by  $\beta$ -reduction, we also need a way to generate new names on which to communicate. To circumvent these problems and illustrate the expressiveness of HOcore, we consider encodings where the *reduction strategy* is fixed, thus for which at most one redex is enabled at any time. In this setting,  $\beta$ -reduction can be emulated using communication on a single, shared, name. A first contribution of this paper is a novel encoding of the call-by-name  $\lambda$ -calculus, or more precisely the Krivine Abstract Machine (KAM) [14], into HOcore.

A faithful encoding not only reflects the operational semantics of a calculus, it should also reflect its equivalences. Ideally, an encoding is *fully abstract*: two source terms are behaviorally equivalent iff their translations are equivalent. On the HOcore side, we use *barbed equivalence with hidden names* [16], where a fixed number of names used for the translation cannot be observed. On the  $\lambda$ -calculus side, we consider three equivalences. First, we look at *normal-form bisimilarity* [17], where normal forms are decomposed into subterms that must be bisimilar. Next, we turn to *applicative bisimilarity* [1], where normal forms must behave similarly when applied to identical arguments. And finally, we consider

contextual equivalence, where terms must behave identically when put into arbitrary contexts. Our second contribution is an *internalization* of these equivalences into extended abstract machines: these machines expand the KAM, which performs the evaluation of a term, with additional transitions with flags interpreting the equivalences. By doing so, we can express these different equivalences on terms by a simpler bisimilarity on these flags-generating machines, which can be seen as labeled transition systems (LTS). Finally, we translate these extended machines into HOcore and prove full abstraction for all three equivalences. Altogether, this work shows that a minimal process calculus with no name restriction can faithfully encode the call-by-name  $\lambda$ -calculus.

*The chosen equivalences:* We study normal-form and applicative bisimilarities, even though faithfully encoding contextual equivalence is enough to get full abstraction. Our motivation is twofold. First, we start with normal-form bisimilarity because it is the simplest to translate, as we do not need to inject terms from the environment to establish the equivalence. We next show how we can inject terms for applicative bisimilarity, and we then extend this approach to contexts for contextual equivalence. Second, the study of quite different equivalences illustrate the robustness of the internalization technique.

*Related work:* Since Milner’s seminal work [18], other encodings of  $\lambda$  into  $\pi$  have been proposed either as a result itself [9], or to study other properties such as connections with logic [2, 5, 22], termination [10, 4, 24], sequentiality [6], control [10, 13, 23], or Continuation-Passing Style (CPS) transforms [20, 21, 11]. These works use the more expressive *first-order*  $\pi$ -calculus, except for [20, 21], discussed below; full abstraction is proved w.r.t. contextual equivalence in [6, 24, 13], normal-form bisimilarity in [23], applicative bisimilarity in [9], and both bisimilarities in [21]. The encodings of [6, 24, 13] are driven by types, and therefore cannot be compared to our untyped setting. In [23], van Bakel et al. establish a full abstraction result between the  $\lambda\mu$ -calculus with normal-form bisimilarity and the  $\pi$ -calculus. Their encoding relies on an unbounded number of restricted names to evaluate several translations of  $\lambda$ -terms in parallel, while we rely on flags and on barbed equivalence. We explain the differences between the two approaches in Section IV-B. The encoding of [9] also uses an unbounded number of restricted names, to represent a  $\lambda$ -term as a tree and to process it.

Sangiorgi translates the  $\lambda$ -calculus into a higher-order calculus as an intermediary step in [20, 21], but it is an abstraction-passing calculus, which is strictly more expressive than a

process-passing calculus [15]. Like in our work, Sangiorgi fixes the evaluation strategy in the  $\lambda$ -calculus, except that he uses CPS translations rather than abstract machines. In the light of Danvy et al.'s functional correspondence [3], the two approaches appear closely related, however it is difficult to compare our encoding with Sangiorgi's, since we target different calculi, and we internalize the bisimilarities in the abstract machines. Still, name restriction plays an important role in Sangiorgi's encodings, since a local channel is used for each application in a  $\lambda$ -term. The encoding is fully abstract w.r.t. normal-form bisimilarity [21, Chapter 18] but not w.r.t. applicative bisimilarity [21, Chapter 17]. Indeed, a translated  $\lambda$ -abstraction waits for the outside to provide an access to an encoded argument to be applied to. However, the environment may give access to a random process and not a translated  $\lambda$ -term. The encoding of [21] does not protect itself against this unwanted behavior from the environment. In contrast, the encoding of Section V and the one in [9] are fully abstract w.r.t. applicative bisimilarity, because they take this issue into account, as we explain in Section V-B.

*Outline:* Section II presents HOCORE. Section III shows how to encode the KAM; the machine and its encoding are then modified to get full abstraction with relation to normal-form bisimilarity (Section IV) and applicative bisimilarity (Section V). Section VI concludes this paper, and an accompanying research report [8] contains the main proofs.

## II. THE CALCULUS HOCORE

*Syntax and semantics:* HOCORE [16] is a simpler version of HO $\pi$  [19] where name restriction is removed. We let  $a, b, \text{etc.}$  range over channel names, and  $x, y, \text{etc.}$  range over process variables. The syntax of HOCORE processes is:

$$P, Q ::= a(x).P \mid \bar{a}(P) \mid P \parallel Q \mid x \mid \mathbf{0}.$$

The process  $a(x).P$  is waiting for a message on  $a$  which, when received, is substituted for the variable  $x$  in  $P$ . If  $x$  does not occur in  $P$ , then we write  $a(\_).P$ . The process  $\bar{a}(P)$  is sending a message on  $a$ . Note that communication is higher order—processes are sent—and asynchronous—there is no continuation after a message output. The parallel composition of processes is written  $P \parallel Q$ , and the process  $\mathbf{0}$  cannot perform any action. Input has a higher precedence than parallel composition, e.g., we write  $a(x).P \parallel Q$  for  $(a(x).P) \parallel Q$ . We implicitly consider that parallel composition is associative, commutative, and has  $\mathbf{0}$  as a neutral element. In an input  $a(x).P$ , the variable  $x$  is bound in  $P$ . We write  $\text{fn}(P)$  for the channel names of  $P$ .

Informally, when an output  $\bar{a}(P)$  is in parallel with an input  $a(x).Q$ , a communication on  $a$  takes place, producing  $[P/x]Q$ , the capture avoiding substitution of  $x$  by  $P$  in  $Q$ . We define in Figure 1 the semantics of HOCORE as a LTS, omitting the rules symmetric to PAR and TAU. The labels (ranged over by  $l$ ) are either  $\tau$  for internal communication,  $\bar{a}(P)$  for message output, or  $a(P)$  for process input. We use label  $a$  for an input where the received process does not matter (e.g.,  $a(\_).P$ ).

Weak transitions allow for internal actions before and after a visible one. We write  $\xRightarrow{\tau}$  for the reflexive and transitive closure  $\xrightarrow{\tau}^*$ , and  $\xRightarrow{l}$  for  $\xrightarrow{l} \xrightarrow{\tau} \xrightarrow{l} \xrightarrow{\tau}$  when  $l \neq \tau$ .

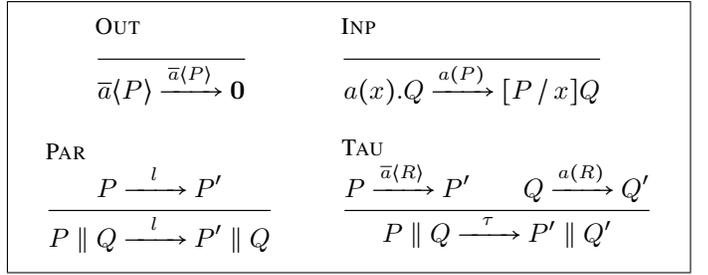


Fig. 1: HOCORE LTS

*Barbed equivalence:* We let  $\gamma$  range over names  $a$  and  $\text{conames } \bar{a}$ ; we define observable actions as follows.

**Definition 1.** *The process  $P$  has a strong observable action on  $a$  (resp.  $\bar{a}$ ), written  $P \downarrow_a$  (resp.  $P \downarrow_{\bar{a}}$ ), if  $P \xrightarrow{a(Q)} R$  (resp.  $P \xrightarrow{\bar{a}(Q)} R$ ) for some  $Q, R$ . A process  $P$  has a weak observable action on  $\gamma$ , written  $P \Downarrow_{\gamma}$ , if  $P \xRightarrow{\tau} P' \downarrow_{\gamma}$  for some  $P'$ . We write  $\text{WkObs}(P)$  for the set of weak observable actions of  $P$ .*

Our definition of barbed equivalence depends on a set  $\mathbb{H}$  of hidden names, which allows some observable actions to be ignored. Instead of adding top-level name restrictions on these names, as in [16], we prefer to preserve the semantics of the calculus and simply hide some names in the equivalence. Hidden names are not a computational construct and are not required for the encoding of the KAM, but they are necessary to protect the encoding from an arbitrary environment when proving full abstraction. We emphasize that we do not need the full power of name restriction: the set of hidden names is finite and static—there is no way to create new hidden names.

**Definition 2.** *A symmetric relation  $\mathcal{R}$  is a barbed bisimulation w.r.t.  $\mathbb{H}$  if  $P \mathcal{R} Q$  implies*

- $P \downarrow_{\gamma}$  and  $\gamma \notin \mathbb{H}$  implies  $Q \downarrow_{\gamma}$ ;
- for all  $R$  such that  $\text{fn}(R) \cap \mathbb{H} = \emptyset$ , we have  $P \parallel R \mathcal{R} Q \parallel R$ ;
- if  $P \xrightarrow{\tau} P'$ , then there exists  $Q'$  such that  $Q \xRightarrow{\tau} Q'$  and  $P' \mathcal{R} Q'$ .

*Barbed equivalence w.r.t.  $\mathbb{H}$ , noted  $\approx_{\text{HO}}^{\mathbb{H}}$ , is the largest barbed bisimulation w.r.t.  $\mathbb{H}$ .*

A strong barbed equivalence can be defined by replacing  $\Downarrow_{\gamma}$  with  $\downarrow_{\gamma}$  in the first item, and  $\xRightarrow{\tau}$  with  $\xrightarrow{\tau}$  in the third. From [16], we know that strong barbed equivalence is decidable when  $\mathbb{H} = \emptyset$ , but undecidable when  $\mathbb{H}$  is of cardinal at least 4. We lower this bound to 2 in Theorem 4.

## III. ENCODING THE KRIVINE ABSTRACT MACHINE

We show in this section that HOCORE may faithfully encode a call-by-name  $\lambda$ -calculus through an operationally equivalent encoding of the KAM.

### A. Definition of the KAM

The KAM [14] is a machine for call-by-name evaluation of closed  $\lambda$ -calculus terms. We present a substitution-based variant of the KAM for simplicity, and to reuse the substitution

of HOCORE in the translation. A *configuration*  $C$  of the machine is composed of the term  $t$  being evaluated, and a stack  $\pi$  of  $\lambda$ -terms. Their syntax and the transitions are as follows.

$$\begin{aligned}
C &::= t * \pi && \text{(configurations)} \\
t, s &::= x \mid tt \mid \lambda x.t && \text{(terms)} \\
\pi &::= t :: \pi \mid [] && \text{(stacks)} \\
t * \pi &\mapsto t * s :: \pi && \text{(PUSH)} \\
\lambda x.t * s :: \pi &\mapsto [s/x]t * \pi && \text{(GRAB)}
\end{aligned}$$

A  $\lambda$ -abstraction  $\lambda x.t$  binds  $x$  in  $t$ ; a term is closed if it does not contain any free variables. We use  $[]$  to denote the empty stack. In PUSH, the argument  $s$  of an application is stored on the stack while the term  $t$  in function position is evaluated. If we get a  $\lambda$ -abstraction  $\lambda x.t$ , then an argument  $s$  is fetched from the stack (transition GRAB), and the evaluation continues with  $[s/x]t$ . If a configuration of the form  $\lambda x.t * []$  is reached, then the evaluation is finished, and the result is  $\lambda x.t$ . Because we evaluate closed terms only, it is not possible to obtain a configuration of the form  $x * \pi$ .

## B. Translation into HOCORE

The translation of the KAM depends essentially on how we push and grab terms on the stack. We represent the stack by two messages, one on name  $hd_c$  for its head, and one on name  $c$  (for *continuation*) for its tail (henceforth, a stack  $n$  is always encoded as a message on  $hd_n$  for its head and one on  $n$  for its tail). The empty stack can be represented by an arbitrary, non-diverging, deterministic process, e.g.,  $\mathbf{0}$ ; here we use a third name to signal that the computation is finished with  $\bar{b}(\mathbf{0})$ . As an example, the stack  $1 :: 2 :: 3 :: 4 :: []$  is represented by  $\overline{hd_c}(1) \parallel \bar{c}(\overline{hd_c}(2) \parallel \bar{c}(\overline{hd_c}(3) \parallel \bar{c}(\overline{hd_c}(4) \parallel \bar{c}(\bar{b}(\mathbf{0}))))))$ .

With this representation, pushing an element  $e$  on a stack  $p$  is done by creating the process  $\overline{hd_c}(e) \parallel \bar{c}(p)$ , while grabbing the head of the stack corresponds to receiving on  $hd_c$ . With this idea in mind, we define the translations for the stacks, terms, and configurations as follows, where we assume the variable  $p$  does not occur in the translated entities.

$$\begin{aligned}
\llbracket t * \pi \rrbracket &\hat{=} \llbracket t \rrbracket \parallel \bar{c}(\llbracket \pi \rrbracket) \\
\llbracket [] \rrbracket &\hat{=} \bar{b}(\mathbf{0}) \\
\llbracket t :: \pi \rrbracket &\hat{=} \overline{hd_c}(\llbracket t \rrbracket) \parallel \bar{c}(\llbracket \pi \rrbracket) \\
\llbracket t s \rrbracket &\hat{=} c(p).(\llbracket t \rrbracket \parallel \bar{c}(\overline{hd_c}(\llbracket s \rrbracket) \parallel \bar{c}(p))) \\
\llbracket \lambda x.t \rrbracket &\hat{=} c(p).(hd_c(x). \llbracket t \rrbracket \parallel p) \\
\llbracket x \rrbracket &\hat{=} x
\end{aligned}$$

In the translation of a configuration  $t * \pi$ , we reuse the name  $c$  to store the stack, meaning that before pushing on  $\pi$  or grabbing the head of  $\pi$ , we have to get  $\llbracket \pi \rrbracket$  by receiving on  $c$ . For instance, in the application case  $\llbracket t s \rrbracket$ , we start by receiving the current stack  $p$  on  $c$ , and we then run  $\llbracket t \rrbracket$  in parallel with the translation of the new stack  $\overline{hd_c}(\llbracket s \rrbracket) \parallel \bar{c}(p)$ . Similarly, in the  $\lambda$ -abstraction case  $\llbracket \lambda x.t \rrbracket$ , we get the current stack  $p$  on  $c$ , that we run in parallel with  $hd_c(x). \llbracket t \rrbracket$ . If  $p$  is not empty, then it is a process of the form  $\overline{hd_c}(\llbracket s \rrbracket) \parallel \bar{c}(\llbracket \pi \rrbracket)$ , and a communication

on  $hd_c$  is possible, realizing the substitution of  $x$  by  $s$  in  $t$ ; the execution then continues with  $\llbracket [s/x]t \rrbracket \parallel \bar{c}(\llbracket \pi \rrbracket)$ . Otherwise,  $p$  is  $\bar{b}(\mathbf{0})$ , and the computation terminates.

Formally, the operational correspondence between the KAM and its translation is as follows.

**Theorem 3.** *In the forward direction, if  $C \mapsto^* C'$ , then  $\llbracket C \rrbracket \xrightarrow{\tau} \llbracket C' \rrbracket$ . In the backward direction, if  $\llbracket C \rrbracket \xrightarrow{\tau} P$ , then there exists a  $C'$  such that  $C \mapsto^* C'$  and either*

- $P = \llbracket C' \rrbracket$ ,
- or there exists  $P'$  such that  $P \xrightarrow{\tau} P' = \llbracket C' \rrbracket$ ,
- or  $C' = \lambda x.t * []$  and  $P = hd_c(x). \llbracket t \rrbracket \parallel \bar{b}(\mathbf{0})$ .

*Sketch.* The proof is straightforward in the forward direction. In the backward direction, we show that the translation is deterministic (if  $\llbracket C \rrbracket \xrightarrow{\tau} P \xrightarrow{\tau} Q_1$  and  $\llbracket C \rrbracket \xrightarrow{\tau} P \xrightarrow{\tau} Q_2$ , then  $Q_1 = Q_2$ ) and we rely on the fact that the translation of a PUSH step uses one communication, while we use two communications for a GRAB step.  $\square$

We can then improve over the result of [16] about undecidability of strong barbed equivalence by hiding  $hd_c$  and  $c$ .

**Theorem 4.** *Strong barbed equivalence is undecidable in HOCORE with 2 hidden names.*

*Proof.* Assume we can decide strong barbed congruence with two hidden names, and let  $t$  be a closed  $\lambda$ -term. We can thus decide if  $\llbracket t * [] \rrbracket$  is strong barbed-congruent to  $c(x).(x \parallel \bar{c}(x)) \parallel \bar{c}(c(x).(x \parallel \bar{c}(x)))$  when  $hd_c$  and  $c$  are hidden. As the second term loops with no barbs, deciding congruence is equivalent to deciding whether the reduction of  $t$  converges, hence a contradiction.  $\square$

## IV. NORMAL-FORM BISIMILARITY

Our first full abstraction result is for normal-form bisimilarity [17]. We show how to internalize this equivalence in an extension of the KAM such that it may be captured by a simple barbed bisimilarity. We then translate this extended KAM into HOCORE, and we finally prove full abstraction.

### A. Normal-Form Bisimilarity

Normal-form bisimilarity compares terms by reducing them to weak head normal forms, if they converge, and then decomposes these normal forms into subterms that must be bisimilar. Unlike the KAM, normal-form bisimilarity is defined on open terms, thus we distinguish free variables, ranged over by  $\alpha$ , from bound variables, ranged over by  $x$ . The grammars of terms ( $t, s$ ) and values ( $v$ ) become as follows.

$$t ::= \alpha \mid x \mid \lambda x.t \mid tt \quad v ::= \alpha \mid \lambda x.t$$

Henceforth, we assume that  $\lambda$ -terms are well formed, i.e., all variables ranged over by  $x$  are bound:  $x$  is not a valid term but  $\alpha$  is. We write  $\text{fv}(t)$  for the set of free variables of  $t$ . A variable  $\alpha$  is said *fresh* if it does not occur in any entities under consideration.

When evaluating an open term, we can obtain either a  $\lambda$ -abstraction, or a free variable in a stack. We inductively extend a relation  $\mathcal{R}$  on  $\lambda$ -terms to stacks by writing  $\pi \mathcal{R} \pi'$  if  $\pi = \pi' = []$ , or if  $\pi_1 = t :: \pi'_1$ ,  $\pi_2 = s :: \pi'_2$ ,  $t \mathcal{R} s$ , and  $\pi'_1 \mathcal{R} \pi'_2$ .

**Definition 5.** A symmetric relation  $\mathcal{R}$  is a normal-form bisimulation if  $t \mathcal{R} s$  implies:

- if  $t \star [] \mapsto^* \lambda x. t' \star []$ , then there exists  $s'$  such that  $s \star [] \mapsto^* \lambda x. s' \star []$  and  $[\alpha / x] t' \mathcal{R} [\alpha / x] s'$  for a fresh  $\alpha$ ;
- if  $t \star [] \mapsto^* \alpha \star \pi$ , then there exists  $\pi'$  such that  $s \star [] \mapsto^* \alpha \star \pi'$  and  $\pi \mathcal{R} \pi'$ .

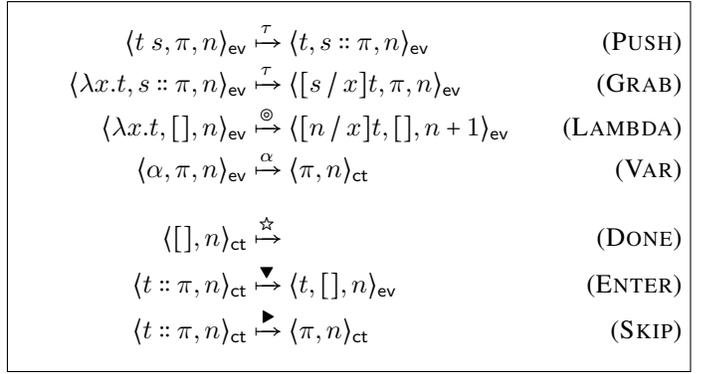
Normal-form bisimilarity  $\approx_{\text{nf}}$  is the largest normal-form bisimulation.

### B. Abstract Machine

We now extend the KAM so that it provides additional steps, identified by labeled transitions, that capture the testing done by normal-form bisimilarity. This extended machine features *flagged transitions*, *terminating transitions*, and a restricted form of *non-determinism*. Flagged transitions are usual transitions of the machine with some additional information to convey to the environment that a particular event is taking place. Machine bisimilarity, defined below, ensures that bisimilar machines have matching flags. Transitions without flags use a  $\tau$  label. Terminating transitions are flagged transitions that indicate the computation has stopped. They are needed for bisimilarity: as machine bisimilarity ignores  $\tau$  labels, we use terminating transitions to distinguish between terminated and divergent machine runs. Finally, we allow non-determinism in machines, i.e., a given configuration may take two different transitions, only if the transitions are flagged and have different flags. In other words, the non-deterministic choice is made explicit to the environment.

We define the NFB machine in Figure 2. When computation stops with a  $\lambda$ -abstraction and an empty stack, we have to restart the machine to evaluate the body of the abstraction with a freshly generated free variable (rule LAMBDA). To do so, we consider free variables as natural numbers, and we keep a counter  $n$  in the machine which is incremented each time a fresh variable is needed. For a configuration  $\alpha \star \pi$ , normal-form bisimilarity evaluates each of the  $t_i$  in the stack (rule VAR). To internalize this step, we could launch several machines in parallel, as in [23], where the translated  $t_i$  are run in parallel. This approach has two drawbacks: first, it is a further extension of abstract machines (a machine no longer steps to a single machine state but to a multiset of states). Second, when translating such extended machines into HOcore, we want to prevent them from interacting with each other, but we cannot rely on name restriction, as in [23], to encapsulate an unbounded number of translations. Alternatively, one could evaluate the elements of the stack sequentially, but this approach fails if one of the elements of the stack diverges, as the later elements will never be evaluated. We thus consider a third approach, built upon flagged non-determinism: the machine chooses arbitrarily an element of the stack to evaluate, and signals this choice using flags (rules ENTER, SKIP, and DONE). The burden of evaluating every element of the stack is thus relegated to the definition of machine bisimilarity: as every flagged execution must be matched by an execution with the same flags, every possible choice is explored.

As before, we use  $C$  to range over configurations, which are now of two kinds. In *evaluation mode*,  $\langle t, \pi, n \rangle_{\text{ev}}$  is reducing  $t$



**Fig. 2:** NFB Machine

within stack  $\pi$  and with counter  $n$ . The transitions PUSH and GRAB are as in the KAM, except for the extra parameter. If we reach a  $\lambda$ -abstraction in the empty context (transition LAMBDA), then the machine flags  $\odot$  and then restarts to evaluate the body, replacing the bound variable by a fresh free variable, i.e., the current value  $n$  of the counter. If we reach a free variable  $\alpha$ , i.e., a number, then we flag the value of  $\alpha$  before entering the next mode (transition VAR).

In *continuation mode*  $\langle \pi, n \rangle_{\text{ct}}$ , the transition DONE simply finishes the execution if  $\pi = []$ , using the flag  $\star$ . Otherwise,  $\pi = t :: \pi'$ , and the machine either evaluates  $t$  with flag  $\blacktriangledown$  (and forgets about  $\pi'$ ), or skips  $t$  with a flag  $\blacktriangleright$  to possibly evaluate a term in  $\pi'$ . The machine may skip the evaluation of all the terms in  $\pi$ , but it would still provide some information, as it would generate  $m$   $\blacktriangleright$  messages (followed by  $\star$ ), telling us that  $\pi$  has  $m$  elements. Note that the counter  $n$  is stored in continuation mode just to be passed to the evaluation mode when one of the  $t_i$  is chosen with transition ENTER.

**Example 6.** To illustrate how the machine works, we show the transitions starting from the term  $(\lambda x. x) (\lambda y. y \ 0 \ \Omega)$ , where  $\Omega \triangleq (\lambda x. x \ x) (\lambda x. x \ x)$ . The term is executed in the empty context, and with a counter initialized to a value greater than its free variables.

$$\begin{aligned}
 & \langle (\lambda x. x) (\lambda y. y \ 0 \ \Omega), [], 1 \rangle_{\text{ev}} \\
 & \xrightarrow{\tau} \langle \lambda x. x, \lambda y. y \ 0 \ \Omega :: [], 1 \rangle_{\text{ev}} && \text{(PUSH)} \\
 & \xrightarrow{\tau} \langle \lambda y. y \ 0 \ \Omega, [], 1 \rangle_{\text{ev}} && \text{(GRAB)} \\
 & \xrightarrow{\odot} \langle 1 \ 0 \ \Omega, [], 2 \rangle_{\text{ev}} && \text{(LAMBDA)} \\
 & \xrightarrow{\tau} \langle 1 \ 0, \Omega :: [], 2 \rangle_{\text{ev}} \xrightarrow{\tau} \langle 1, 0 :: \Omega :: [], 2 \rangle_{\text{ev}} && \text{(PUSH - PUSH)} \\
 & \xrightarrow{1} \langle 0 :: \Omega :: [], 2 \rangle_{\text{ct}} && \text{(VAR)}
 \end{aligned}$$

We then have three possibilities. First, we reduce the top of the stack, with the sequence  $\langle 0 :: \Omega :: [], 2 \rangle_{\text{ct}} \xrightarrow{\blacktriangledown} \langle 0, [], 2 \rangle_{\text{ev}} \xrightarrow{\odot} \langle [], 2 \rangle_{\text{ct}} \xrightarrow{\star}$ . Second, we evaluate  $\Omega$  with the sequence  $\langle 0 :: \Omega :: [], 2 \rangle_{\text{ct}} \xrightarrow{\blacktriangleright} \langle \Omega :: [], 2 \rangle_{\text{ct}} \xrightarrow{\blacktriangledown} \langle \Omega, [], 2 \rangle_{\text{ev}}$ , and then the machine loops without generating any flag. Third, we skip both terms with  $\langle 0 :: \Omega :: [], 2 \rangle_{\text{ct}} \xrightarrow{\blacktriangleright} \langle \Omega :: [], 2 \rangle_{\text{ct}} \xrightarrow{\blacktriangleright} \langle [], 2 \rangle_{\text{ct}} \xrightarrow{\star}$ . Note that the three options generate different traces of flags.

Because the rules GRAB and PUSH are the same between the KAM and the NFB machine, there is a direct correspondence between the two.

**Lemma 7.** For all  $t, t', \pi, \pi', n, t \star \pi \mapsto t' \star \pi'$  iff  $\langle t, \pi, n \rangle_{\text{ev}} \xrightarrow{\tau} \langle t', \pi', n \rangle_{\text{ev}}$ .

We finally show that a notion of bisimilarity between configurations of an NFB machine captures normal-form bisimilarity. To this end, we first define machine bisimilarity, where we denote the flags of the machine by  $f$ .

**Definition 8.** A symmetric relation  $\mathcal{R}$  is a machine bisimulation if  $C_1 \mathcal{R} C_2$  implies:

- if  $C_1 \xrightarrow{\tau}^* \xrightarrow{f} C'_1$ , then there exists  $C'_2$  such that  $C_2 \xrightarrow{\tau}^* \xrightarrow{f} C'_2$  and  $C'_1 \mathcal{R} C'_2$ ;
- if  $C_1 \xrightarrow{\tau}^* \xrightarrow{f}$ , then  $C_2 \xrightarrow{\tau}^* \xrightarrow{f}$ .

Machine bisimilarity  $\approx_m$  is the largest machine bisimulation.

Intuitively, machine bisimilarity ensures that every flag emitted by a machine is matched by an identical flag from the other machine, up to internal reductions. Note that a machine that diverges with  $\tau$  labels can be related to any other diverging machine or any machine stuck without a flag. We make sure the latter case cannot occur in our machines by having only terminating transitions, which are flagged, as stuck transitions. We can now state that normal-form bisimilarity coincides with machine bisimilarity of NFB machines.

**Theorem 9.** We have  $t \approx_{\text{nf}} s$  iff there exists  $n > \max(\text{fv}(t) \cup \text{fv}(s))$  such that  $\langle t, [], n \rangle_{\text{ev}} \approx_m \langle s, [], n \rangle_{\text{ev}}$ .

*Sketch.* Machine bisimilarity implies  $\approx_{\text{nf}}$  because

$\{(t, s) \mid \langle t, [], n \rangle_{\text{ev}} \approx_m \langle s, [], n \rangle_{\text{ev}}, n > \max(\text{fv}(t) \cup \text{fv}(s))\}$  is a normal-form bisimulation, and the other direction is by showing that

$\{(\langle t, [], n \rangle_{\text{ev}}, \langle s, [], n \rangle_{\text{ev}}) \mid t \approx_{\text{nf}} s, n > \max(\text{fv}(t), \text{fv}(s))\}$   
 $\cup \{(\langle \pi, n \rangle_{\text{ct}}, \langle \pi', n \rangle_{\text{ct}}) \mid \pi \approx_{\text{nf}} \pi', n > \max(\text{fv}(\pi), \text{fv}(\pi'))\}$   
 is a machine bisimulation.  $\square$

### C. Translation into HOcore

In Figure 3, we present the translation of the NFB machine into HOcore, where we consider flags as channel names. Configurations now contain a counter  $n$ , which is represented by a message on  $k$  containing the value of  $n$  encoded as a process. We use  $[\cdot]_{\text{Int}}$  to translate a natural number  $n$  into a process  $\underbrace{\text{succ}(\_)\dots\text{succ}(\_)}_{n \text{ times}}.z(\_).\text{init}(\mathbf{0})$ ; the role of

the final output on  $\text{init}$  is explained later. Free variables  $\alpha$  are also numbers, since we cannot generate new names in HOcore, and we use the same translation for them. We also use non-deterministic internal choice, encoded as follows:  $P + Q \triangleq \overline{ch}\langle P \rangle \parallel \overline{ch}\langle Q \rangle \parallel ch(x).ch(\_).x$ : both messages are consumed, and only one process is executed. This encoding supposes that at most one choice is active at a given time, as we use only one name  $ch$  to encode all the choices. We also use  $n$ -ary choices for  $n > 2$  in Section V-C, which can be encoded in the same way.

A stack is represented as in the KAM, by messages on  $hd_c$  and  $c$ , and the translation of an application  $\llbracket t s \rrbracket$  is exactly the same as for the KAM. The encoding of the empty context  $[\ ]$

$$\begin{aligned}
 \llbracket t s \rrbracket &\triangleq c(p).(\llbracket t \rrbracket \parallel \overline{c}\langle \overline{hd}_c(\llbracket s \rrbracket) \parallel \overline{c}\langle p \rangle \rangle) \\
 \llbracket \lambda x.t \rrbracket &\triangleq c(p).(p \parallel \overline{b}\langle \text{Restart} \rangle \parallel hd_c(x).b(\_).\llbracket t \rrbracket) \\
 \llbracket x \rrbracket &\triangleq x \\
 \llbracket \alpha \rrbracket &\triangleq [\alpha]_{\text{Int}} \\
 \text{Restart} &\triangleq \odot(\_).k(x).\left(\overline{hd}_c\langle x \rangle \parallel \overline{k}\langle \text{succ}(\_).x \rangle \parallel \overline{c}\langle [\ ] \rangle \parallel \overline{b}\langle \mathbf{0} \rangle\right) \\
 \text{Rec} &\triangleq \text{init}(\_).\text{rec}(x).(x \parallel \overline{rec}\langle x \rangle \parallel \text{Cont}) \\
 \text{Cont} &\triangleq c(p).(p \parallel \overline{b}\langle \star(\_).\mathbf{0} \rangle \parallel hd_c(x).b(\_).\text{Chce}(x)) \\
 \text{Chce}(P_t) &\triangleq \blacktriangledown(\_).c(\_).(P_t \parallel \overline{c}\langle [\ ] \rangle) + \blacktriangleright(\_).\text{init}(\mathbf{0}) \\
 [\ ] &\triangleq b(x).x \\
 \llbracket t :: \pi \rrbracket &\triangleq \overline{hd}_c(\llbracket t \rrbracket) \parallel \overline{c}\langle [\pi] \rangle \\
 [\mathbf{0}]_{\text{Int}} &\triangleq z(\_).\text{init}(\mathbf{0}) \\
 \llbracket n + 1 \rrbracket_{\text{Int}} &\triangleq \text{succ}(\_).\llbracket n \rrbracket_{\text{Int}} \\
 \llbracket \langle t, \pi, n \rangle_{\text{ev}} \rrbracket &\triangleq \llbracket t \rrbracket \parallel \overline{c}\langle [\pi] \rangle \parallel \overline{k}\langle [\llbracket n \rrbracket_{\text{Int}}] \rangle \parallel \text{Rec} \parallel \overline{rec}\langle \text{Rec} \rangle \\
 \llbracket \langle \pi, n \rangle_{\text{ct}} \rrbracket &\triangleq \overline{c}\langle [\pi] \rangle \parallel \overline{k}\langle [\llbracket n \rrbracket_{\text{Int}}] \rangle \parallel \text{Cont} \parallel \text{Rec} \parallel \overline{rec}\langle \text{Rec} \rangle
 \end{aligned}$$

**Fig. 3:** Translation of the NFB machine into HOcore

is different, however, because contexts are used to distinguish between execution paths at two points in the machine: when evaluating a function  $\lambda x.t$  in evaluation mode, and when deciding whether the execution is finished in continuation mode. The empty context is thus encoded as  $b(x).x$ , waiting to receive the process to execute in the empty case. For the non-empty case, this input on  $b$  is absent and there are instead messages on  $hd_c$  and  $c$ . Thus the generic way to choose a branch is as follows:

$\overline{b}\langle \text{do this if empty} \rangle \parallel hd_c(x).c(y).b(\_).\text{do this if non-empty}$ .  
 In the non-empty case, the input on  $b$  discards the message for the empty behavior that was not used.

For  $\lambda$ -abstractions, the behavior for the empty case is described in the process Restart. More precisely,  $\llbracket \lambda x.t \rrbracket$  receives the current stack  $[\pi]$  on  $c$  to run it in parallel with  $\overline{b}\langle \text{Restart} \rangle \parallel hd_c(x).b(\_).\llbracket t \rrbracket$ . If  $[\pi]$  is of the form  $\overline{hd}_c(\llbracket t' \rrbracket) \parallel \overline{c}\langle [\pi'] \rangle$ , then we have the same behavior as with the KAM, with an extra communication on  $b$  to garbage collect the Restart process. Otherwise,  $[\pi] = b(x).x$  and we obtain the following sequence of transitions.

$$\begin{aligned}
 &b(x).x \parallel \overline{b}\langle \text{Restart} \rangle \parallel hd_c(x).b(\_).\llbracket t \rrbracket \parallel \overline{k}\langle [\llbracket n \rrbracket_{\text{Int}}] \rangle \\
 \xrightarrow{\tau} &\odot(\_).k(x).\left(\overline{hd}_c\langle x \rangle \parallel \overline{k}\langle \text{succ}(\_).x \rangle \parallel \overline{c}\langle [\ ] \rangle \parallel \overline{b}\langle \mathbf{0} \rangle\right) \\
 &\parallel hd_c(x).b(\_).\llbracket t \rrbracket \parallel \overline{k}\langle [\llbracket n \rrbracket_{\text{Int}}] \rangle \\
 \xRightarrow{\odot} &\overline{hd}_c\langle [\llbracket n \rrbracket_{\text{Int}}] \rangle \parallel \overline{k}\langle \text{succ}(\_).\llbracket n \rrbracket_{\text{Int}} \rangle \parallel \overline{c}\langle [\ ] \rangle \parallel \overline{b}\langle \mathbf{0} \rangle \\
 &\parallel hd_c(x).b(\_).\llbracket t \rrbracket \\
 \xrightarrow{\tau} &\overline{k}\langle \text{succ}(\_).\llbracket n \rrbracket_{\text{Int}} \rangle \parallel \overline{c}\langle [\ ] \rangle \parallel \overline{b}\langle \mathbf{0} \rangle \parallel b(\_).\llbracket [n/x]t \rrbracket \\
 \xrightarrow{\tau} &\overline{k}\langle [\llbracket n + 1 \rrbracket_{\text{Int}}] \rangle \parallel \overline{c}\langle [\ ] \rangle \parallel \llbracket [n/x]t \rrbracket
 \end{aligned}$$

In the end, we have effectively restarted the machine to evaluate  $[n/x]t$ , as wished.

In continuation mode, the branching is done by the process

Cont, which is executed after applying the transition VAR. More precisely, a free variable  $\alpha$  is translated using  $\llbracket \cdot \rrbracket_{\text{Int}}$ , which signals first the value of  $\alpha$  (with the names  $\text{suc}$  and  $z$ ), and then sends a message on  $\text{init}$  to enter the continuation mode. The way the NFB machine chooses which  $t_i$  to evaluate in a stack  $t_1 :: \dots :: t_m :: []$  is a recursive mechanism, and recursion can be encoded in a higher-order calculus:  $\text{Rec} \parallel \overline{\text{rec}}(\text{Rec})$  reduces to  $\text{Cont} \parallel \text{Rec} \parallel \overline{\text{rec}}(\text{Rec})$  when it receives a message on  $\text{init}$ . The process Cont is doing a case analysis on  $\llbracket \pi \rrbracket$  when it is executed in parallel with  $\overline{c}(\llbracket \pi \rrbracket)$ : if  $\pi = []$ , then  $\llbracket \pi \rrbracket = b(x).x$  receives the message on  $b$  which flags  $\star$  and the machine stops. Otherwise,  $\llbracket \pi \rrbracket = \overline{hd}_c(\llbracket t \rrbracket) \parallel \overline{c}(\llbracket \pi' \rrbracket)$ , and we have the following reductions:

$$\begin{aligned} \text{Cont} \parallel \overline{c}(\llbracket \pi \rrbracket) &\xrightarrow{\tau} \overline{hd}_c(\llbracket t \rrbracket) \parallel \overline{c}(\llbracket \pi' \rrbracket) \parallel \overline{b}(\star(\_)).\mathbf{0} \\ &\quad \parallel \overline{hd}_c(x).b(\_).\text{Chce}(x) \\ &\xrightarrow{\tau}^2 \overline{c}(\llbracket \pi' \rrbracket) \parallel \text{Chce}(\llbracket t \rrbracket) \end{aligned}$$

At this point,  $\text{Chce}(\llbracket t \rrbracket)$  either evaluates  $t$  with flag  $\blacktriangledown$ , or flags  $\blacktriangleright$  and continues exploring  $\pi'$ . In the former case, the current stack  $\pi'$  is replaced by an empty stack, and in the latter, a message on  $\text{init}$  is issued to produce  $\text{Cont} \parallel \overline{c}(\llbracket \pi' \rrbracket)$  after some reduction steps.

#### D. Operational Correspondence and Full Abstraction

Establishing full abstraction requires first to state the correspondence between the NFB machine and its translation. In what follows, we let  $f$  range over the flags  $\odot$ ,  $\blacktriangledown$ ,  $\blacktriangleright$ ,  $\star$ , and  $\alpha$ , where  $\alpha$  is used as a shorthand for the succession of  $\alpha$   $\text{suc}$  flags followed by a  $z$  flag. We let  $\hat{f}$  range over flags and  $\tau$ .

**Definition 10.** A process  $P$  is a machine process if there exists a configuration  $C$  of the machine such that  $\llbracket C \rrbracket \xrightarrow{\tau} P$ .

To establish the correspondence between the machine and its translation, we first define a predicate  $\text{next}(\hat{f}, P)$  such that, if  $Q \in \text{next}(\hat{f}, P)$ , then there is a weak reduction with a step  $\hat{f}$  from  $P$  to  $Q$ ,  $Q$  is the translation of a machine, and there is no machine translation in between. Intuitively, it is the first translation reached after a step  $\hat{f}$ .

**Definition 11.** We write  $Q \in \text{next}(\hat{f}, P)$  if  $P \xrightarrow{\tau} \xrightarrow{\hat{f}} \xrightarrow{\tau} Q$ ,  $Q = \llbracket C' \rrbracket$  for some  $C'$ , and for any  $P'$  such that  $P' \neq P$ ,  $P' \neq Q$ , and  $P \xrightarrow{\hat{f}} P' \xrightarrow{\tau} Q$  or  $P \xrightarrow{\tau} P' \xrightarrow{\hat{f}} Q$ , we have  $P' \neq \llbracket C \rrbracket$  for any  $C$ .

A machine process is either a translation of a configuration, or an intermediary state between two translations;  $\text{next}(\hat{f}, P)$  gives the set of next processes which are translations. We now prove that, with a single exception, the translation is deterministic. The exception to determinism corresponds to the choice made in continuation mode, which is also not deterministic (but flagged) in the machine. We call *choice process* a process about to make that choice; such processes are of the form  $\text{Chce}(P_t, P_\pi, P_n) \hat{=} \text{Chce}(P_t) \parallel \overline{c}(P_\pi) \parallel \overline{k}(P_n) \parallel \text{Rec} \parallel \overline{\text{rec}}(\text{Rec})$ .

**Lemma 12.** Let  $P$  be a machine process which is not a choice process. If  $P \xrightarrow{\hat{f}} P'$  and  $P \xrightarrow{\hat{f}'} P''$ , then  $\hat{f} = \hat{f}'$  and  $P' =$

$P''$ . Let  $P$  be a choice process. If  $P \xrightarrow{\tau} P'$ ,  $P \xrightarrow{\tau} P''$ , and  $\text{WkObs}(P') = \text{WkObs}(P'')$ , then  $P' = P''$ .

The choice process  $\text{Chce}(P_t, P_\pi, P_n)$  may only reduce to the translation of a configuration after a flag  $\blacktriangleright$  or  $\blacktriangledown$ . Thus  $\text{next}(\tau, \text{Chce}(P_t, P_\pi, P_n))$  is empty, and Lemma 12 implies that  $\text{next}(\hat{f}, P)$  is a singleton if it is not empty. In the following, we write  $\text{next}(\hat{f}, P)$  to assert that it is not empty and to directly denote the corresponding unique machine translation.

We can now state the correspondence between the NFB machine and its translation.

**Lemma 13.** The following assertions hold:

- $C \xrightarrow{\hat{f}} C'$  iff  $\llbracket C \rrbracket \xrightarrow{\hat{f}} \llbracket C' \rrbracket$  and  $\text{next}(\hat{f}, \llbracket C \rrbracket) = \llbracket C' \rrbracket$ .
- $C \xrightarrow{\star} P$  iff  $\llbracket C \rrbracket \xrightarrow{\tau} \xrightarrow{\star} P$  and  $P \approx_{\text{HO}} \mathbf{0}$ .

With this result, we can relate the bisimilarities of each calculus. Henceforth, we write  $\approx_{\text{HO}}$  for  $\approx_{\text{HO}}^{\mathbb{H}}$  where  $\mathbb{H}$  contains the names of the translation that are not flags. We define the process complementing a flag as follows: for  $f \neq \alpha$ , we write  $\bar{f}$  for the process  $\bar{f}(\mathbf{0})$ , and we write  $\bar{\alpha}$  for the process defined as  $\bar{\alpha} + 1 \hat{=} \overline{\text{suc}}(\mathbf{0}) \parallel \bar{\alpha}$  and  $\bar{0} \hat{=} \bar{z}(\mathbf{0})$ .

**Theorem 14.**  $C \approx_m C'$  iff  $\llbracket C \rrbracket \approx_{\text{HO}} \llbracket C' \rrbracket$ .

*Sketch.* To prove that barbed equivalence implies machine equivalence, we show that  $\mathcal{R} \hat{=} \{(C, C') \mid \llbracket C \rrbracket \approx_{\text{HO}} \llbracket C' \rrbracket\}$  is a machine bisimulation. Let  $C_1 \mathcal{R} C'_1$ , so that  $C_1 \xrightarrow{\tau}^* C_2 \xrightarrow{f} C_3$ ; then  $\llbracket C_1 \rrbracket \parallel \bar{f} \xrightarrow{\tau} \llbracket C_2 \rrbracket \parallel \bar{f} \xrightarrow{\tau} \llbracket C_3 \rrbracket$  by Lemma 13, which in turn implies that there exists  $P$  such that  $\llbracket C'_1 \rrbracket \parallel \bar{f} \xrightarrow{\tau} P$  and  $\llbracket C_3 \rrbracket \approx_{\text{HO}} P$ . In particular, we have  $\text{WkObs}(\llbracket C_3 \rrbracket) = \text{WkObs}(P)$ , meaning that  $\neg(P \downarrow_{\bar{f}})$ . Consequently, there exists  $P'$  such that  $\llbracket C'_1 \rrbracket \xrightarrow{\tau} P'$  and  $P' \downarrow_{\bar{f}}$ . We can prove that  $P \approx_{\text{HO}} \text{next}(f, P')$ , but by definition of next, there exists  $C'_3$  so that  $\text{next}(f, P') = \llbracket C'_3 \rrbracket$ . As a result, we have  $\llbracket C_3 \rrbracket \approx_{\text{HO}} P \approx_{\text{HO}} \llbracket C'_3 \rrbracket$ , i.e.,  $C_3 \mathcal{R} C'_3$ , and  $\llbracket C'_1 \rrbracket \xrightarrow{f} \llbracket C'_3 \rrbracket$ , which implies  $C'_1 \xrightarrow{\tau}^* \xrightarrow{f} C'_3$ , as wished. The case  $C_1 \xrightarrow{\tau}^* C_2 \xrightarrow{\star} P$  is similar.

For the reverse implication, we prove that

$$\mathcal{R} \hat{=} \left\{ \begin{array}{l} (P_C \parallel R, P_{C'} \parallel R) \mid C \approx_m C', \\ \text{WkObs}(P_C) = \text{WkObs}(P_{C'}) \\ P_C \xrightarrow{\tau} \llbracket C \rrbracket, P_{C'} \xrightarrow{\tau} \llbracket C' \rrbracket \text{ or} \\ \llbracket C \rrbracket \xrightarrow{\tau} P_C, \llbracket C' \rrbracket \xrightarrow{\tau} P_{C'}, \end{array} \right\} \cup \{(P \parallel R, P' \parallel R) \mid P \approx_{\text{HO}} \mathbf{0} \approx_{\text{HO}} P'\}$$

is a barbed bisimulation. The difficult part is to check that  $P_C \parallel R \xrightarrow{\tau} P'$  with a communication on a flag  $f$  is matched by  $P_{C'} \parallel R$ . We have  $P_C \downarrow_{\bar{f}}$ , which implies that  $P_C$  cannot reduce: a machine process is either reducing or has an observable action. We are therefore in the case  $\llbracket C \rrbracket \xrightarrow{\tau} P_C$ , and  $R \downarrow_{\bar{f}}$ . Suppose  $f \neq \star$ ; then  $P_C \xrightarrow{f} P_{C_2} \xrightarrow{\tau} \llbracket C_2 \rrbracket$  for some  $P_{C_2}$  and with  $\llbracket C_2 \rrbracket = \text{next}(f, P_C)$ . Because a machine process is deterministic (Lemma 12), then in fact  $P' = P_{C_2} \parallel R'$  for some  $R'$ . We have  $\llbracket C \rrbracket \xrightarrow{f} \llbracket C_2 \rrbracket$ , so  $C \xrightarrow{\tau}^* \xrightarrow{f} C_2$  holds by Lemma 13. Because  $C \approx_m C'$ ,

there exists  $C'_2$  such that  $C' \xrightarrow{\tau} \xrightarrow{*} \xrightarrow{f} C'_2$  and  $C_2 \approx_m C'_2$ . By Lemma 13, this implies  $\llbracket C' \rrbracket \xrightarrow{f} \llbracket C'_2 \rrbracket$ ; in particular, there exists  $P_{C'_2}$  such that  $\llbracket C' \rrbracket \xrightarrow{\tau} \xrightarrow{f} P_{C'_2} \xrightarrow{\tau} \llbracket C'_2 \rrbracket$ . By Lemma 12, we have  $P_{C'} \xrightarrow{\tau} \xrightarrow{f} P_{C'_2}$ , therefore we have  $P_{C'} \parallel R \xrightarrow{\tau} P_{C'_2} \parallel R'$ , with  $P_{C_2} \parallel R' \mathcal{R} P_{C'_2} \parallel R'$ , as wished. In the case  $f = \star$ , we show that we obtain processes in the second set of  $\mathcal{R}$ .  $\square$

As a result, we can deduce full abstraction between HOcore and the  $\lambda$ -calculus with normal-form bisimilarity.

**Corollary 15.** *We have  $t \approx_{\text{nf}} s$  iff there exists  $n > \max(\text{fv}(t) \cup \text{fv}(s))$  such that  $\llbracket \langle t, [], n \rangle_{\text{ev}} \rrbracket \approx_{\text{HO}} \llbracket \langle s, [], n \rangle_{\text{ev}} \rrbracket$ .*

*Proof.* By Theorems 9 and 14.  $\square$

## V. APPLICATIVE BISIMILARITY

Proving full abstraction w.r.t. normal-form bisimilarity requires minimal interactions—synchronizations on flags—between a machine process and the outside. Achieving full abstraction w.r.t. applicative bisimilarity is intuitively more difficult, since this bisimilarity tests  $\lambda$ -abstractions by applying them to an arbitrary argument. Internalizing such bisimilarity is simple using higher-order flags: one may think of the following transition to test the result of a computation:

$$\lambda x.t \star [] \xrightarrow{s} [s/x]t \star []$$

Although HOcore has higher-order communications, we cannot use them to obtain a fully abstract encoding of such a machine for two reasons. First, allowing interactions where the environment provides a term may allow arbitrary processes to be received, including processes that are not in the image of the translation, thus potentially breaking invariants of the translation. Second, the translation of the KAM has to hide the names it uses for the translation to be fully abstract; it is thus impossible for the context to use such names and to provide translated  $\lambda$ -terms to be tested.

We thus propose in this section to internalize applicative bisimilarity using ordinary flags: when the abstract machine reaches a value, it switches to a different mode where it non-deterministically builds a test term step by step, using flags to indicate its choices so as to ensure that a bisimilar machine builds the same term. The translation of such a machine into HOcore is then similar to the translation of the NFB machine.

Using simple flags to generate terms step by step implies we need to deal with binders. In particular, and anticipating on the HOcore translation, we no longer can rely on the definition of binding and substitution from HOcore, as we cannot write a process that inputs a translation of  $t$  and outputs a translation of  $\lambda x.t$  using an HOcore binding for  $x$ . We thus switch to a pure data description of bindings, using de Bruijn indices. As such terms still need to be executed, we first recall the definition of the KAM with de Bruijn indices and the definitions of contextual equivalence and applicative bisimilarity for  $\lambda$ -terms with de Bruijn indices. We then present the machine internalizing applicative bisimilarity, its translation into HOcore, and show they are fully abstract. We finally conclude this section by showing how contextual

equivalence is internalized in an abstract machine, generating contexts instead of terms.

### A. The KAM and Behavioral Equivalences

In the  $\lambda$ -calculus with de Bruijn indices, a variable is a number that indicates how many  $\lambda$ 's are between the variable and its binder. For example,  $\lambda x.x$  is written  $\lambda.0$  and  $\lambda xy.x y$  is written  $\lambda.\lambda.1 0$ . The syntax of terms  $(t, s)$ , closures  $(\eta)$ , environments  $(e, d)$ , and values  $(v)$  is as follows.

$$t ::= n \mid t s \mid \lambda.t \quad \eta ::= (t, e) \quad e ::= \eta :: e \mid \epsilon \quad v ::= (\lambda.t, e)$$

A closure  $\eta$  is a pair  $(t, e)$  where  $e$  is an environment mapping the free variables of  $t$  to closures; environments are used in lieu of substitutions. A term  $t$  is closed if  $\text{fv}(t) = \emptyset$ , and a closure  $(t, e)$  is closed if the number of elements of  $e$  is bigger than the highest free variable of  $t$ .

The semantics is given by the original, environment-based KAM, where a configuration  $C$  is now composed of the closed closure  $(t, e)$  being evaluated, and a stack  $\pi$  of closures. The transition rules are as follows.

$$\begin{aligned} C ::= \langle t, e, \pi \rangle_{\text{ev}} \text{ (configurations)} \quad \pi ::= \eta :: \pi \mid [] \text{ (stacks)} \\ \langle t s, e, \pi \rangle_{\text{ev}} &\xrightarrow{\tau} \langle t, e, (s, e) :: \pi \rangle_{\text{ev}} && \text{(PUSH)} \\ \langle 0, (t, e) :: d, \pi \rangle_{\text{ev}} &\xrightarrow{\tau} \langle t, e, \pi \rangle_{\text{ev}} && \text{(ZERO)} \\ \langle n+1, (t, e) :: d, \pi \rangle_{\text{ev}} &\xrightarrow{\tau} \langle n, d, \pi \rangle_{\text{ev}} && \text{(ENV)} \\ \langle \lambda.t, e, \eta :: \pi \rangle_{\text{ev}} &\xrightarrow{\tau} \langle t, \eta :: e, \pi \rangle_{\text{ev}} && \text{(GRAB)} \end{aligned}$$

In PUSH, the argument  $s$  of an application is stored on the stack with its environment  $e$  while the term  $t$  in function position is evaluated. If we get a  $\lambda$ -abstraction  $\lambda.t$  (transition GRAB), then an argument  $\eta$  is moved from the stack to the top of the environment to remember that  $\eta$  corresponds to the de Bruijn index 0, and the evaluation continues with  $t$ . Looking up the closure corresponding to a de Bruijn index in the environment is done with the rules ENV and ZERO. Because we evaluate closed closures only, it is not possible to obtain a configuration of the form  $\langle n+1, \epsilon, \pi \rangle_{\text{ev}}$ . If a configuration of the form  $\langle \lambda.t, e, [] \rangle_{\text{ev}}$  is reached, then the evaluation is finished, and the result is  $(\lambda.t, e)$ .

*Behavioral equivalences:* Contextual equivalence compares closed terms by testing them within all contexts. A context  $\mathcal{C}$  is a term with a hole  $\square$  at a variable position; plugging a term  $t$  in  $\mathcal{C}$  is written  $\mathcal{C}[t]$ . A context is closed if  $\text{fv}(\mathcal{C}) = \emptyset$ . Contextual equivalence is then defined as follows.

**Definition 16.** *Two closed terms  $t$  and  $s$  are contextually equivalent, written  $t \approx_{\text{ctx}} s$ , if for all closed contexts  $\mathcal{C}$ ,  $\langle \mathcal{C}[t], \epsilon, [] \rangle_{\text{ev}} \xrightarrow{\tau} \langle \lambda.t', e', [] \rangle_{\text{ev}}$  for some  $t'$  and  $e'$  iff  $\langle \mathcal{C}[s], \epsilon, [] \rangle_{\text{ev}} \xrightarrow{\tau} \langle \lambda.s', d', [] \rangle_{\text{ev}}$  for some  $s'$  and  $d'$ .*

Contextual equivalence is characterized by applicative bisimilarity [1], which reduces closed terms to values that are then applied to an arbitrary argument.

**Definition 17.** *A symmetric relation  $\mathcal{R}$  on closed closures is an applicative bisimulation if  $(t, e) \mathcal{R} (s, d)$  and  $\langle t, e, [] \rangle_{\text{ev}} \xrightarrow{\tau} \langle \lambda.t', e', [] \rangle_{\text{ev}}$  implies that there exist  $s'$  and  $d'$  such that  $\langle s, d, [] \rangle_{\text{ev}} \xrightarrow{\tau} \langle \lambda.s', d', [] \rangle_{\text{ev}}$ , and for all closed  $t''$ , we have  $(t, (t'', \epsilon) :: e) \mathcal{R} (s, (t'', \epsilon) :: d)$ .*

Applicative bisimilarity  $\approx_{\text{app}}$  is the largest applicative bisimulation.

We can prove full abstraction between HOcore and the  $\lambda$ -calculus by either internalizing contextual equivalence or applicative bisimilarity. We choose the latter, as it is closer to normal-form bisimilarity, and we show in Section V-D the machine for contextual equivalence.

### B. Argument Generation for the Applicative Bisimilarity

After evaluating a term thanks to the KAM, we want to produce a closed argument to pass it to the resulting value, and then restart the evaluation process. The approach of [9] consists in waiting for a name from the outside, giving access to a translated  $\lambda$ -term to be applied to. If the environment does not provide access to a well-formed translation, the process simulating  $\beta$ -reduction remains stuck. In contrast, our machine directly generates a well-formed argument: we represent a  $\lambda$ -term as a syntax tree, with de Bruijn indices at the leaves, and applications and  $\lambda$ -abstractions at the nodes. We start generating from the leftmost de Bruijn index, and we then go left to right, meaning that in an application, we create the term in function position before the argument. These choices are completely arbitrary, as doing the opposite—starting from the rightmost index and go right to left—is also possible. To be sure that we produce a valid, closed,  $\lambda$ -term, we have to check that each de Bruijn index  $n$  has at least  $n + 1$   $\lambda$ -abstractions enclosing it, and that each application node has two children.

To do so, we consider machine states with four components: the term  $t$  being constructed, a counter  $\kappa$  giving the minimal number of  $\lambda$ -abstractions required to close the term, a stack  $\rho$  used to build applications, whose syntax is

$$\rho ::= (t, \kappa) :: \rho \mid \odot$$

and which is explained in more detail later, and finally the closure  $\eta$  for which the argument is being built. This last element is never modified by the building process, and is just used to restart the machine in evaluation mode when the argument is finished. We distinguish two kinds of states: the index state  $\langle n, \kappa, \rho, \eta \rangle_{\text{ind}}$ , where only de Bruijn indices can be built, and the term state  $\langle t, \kappa, \rho, \eta \rangle_{\text{tm}}$ , where any term can be produced. The transitions for these states are given in Figure 4.

The transition ARG starts the building process when we reach a  $\lambda$ -abstraction in evaluation mode with the empty continuation  $[]$ . We begin with the index 0, which requires at least 1  $\lambda$ -abstraction above it, and with the empty stack  $\odot$ . The value of the index can then be increased with the transition SUC, which accordingly also increases the value of  $\kappa$ . When we reach the needed value for the index, the transition VAR switches to the term mode; we use two modes to prevent a SUC transition on a term which is not an index.

In term mode, we can add  $\lambda$ -abstractions to the term, decreasing  $\kappa$  if  $\kappa > 0$  with transition LAMBDA, or leaving  $\kappa$  at 0 with transition LAMBDA0; the abstractions we introduce when  $\kappa = 0$  do not bind any variable. Once we are done building a term  $t$  in function position of an application, we use transition APPFUN to build the argument  $s$ . We start again in index mode, but we store on top of  $\rho$  the term  $t$  with its

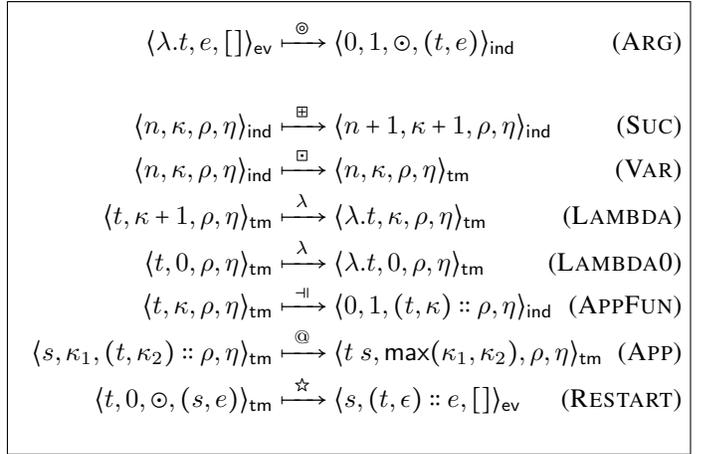


Fig. 4: AB machine: argument generation

counter  $\kappa_2$ . When we finish  $s$  with a counter  $\kappa_1$ , we build the application with transition APP, which takes the maximum of  $\kappa_1$  and  $\kappa_2$  as the new minimal number of  $\lambda$ -abstractions needed above  $t$   $s$ . Note that the APP transition is allowed only if  $\rho$  is not empty, meaning that at least one APPFUN has been done before. Finally, we can conclude the term building process with transition RESTART only if  $\kappa = 0$ , meaning that all the variables of the term are bound, and if  $\rho$  is empty, meaning that there is no application waiting to be finished.

**Example 18.** Figure 5 presents how we generate the term  $\lambda.\lambda.(\lambda.0)$  ( $1 \lambda.0$ ); we start with the underlined 0.

Any closed term  $t$  can be generated with the AB machine, and it is possible to define the sequence of flags  $\text{Seq}(t)$  that will be raised in the process. We write  $()$  for the empty sequence, and  $(f_1, \dots, f_n, (f'_1, \dots, f'_m), f_{n+1}, \dots, f_l)$  for the sequence  $(f_1, \dots, f_n, f'_1, \dots, f'_m, f_{n+1}, \dots, f_l)$ .

**Definition 19.** Given a term  $t$ , we define  $\text{Seq}(t)$  as

$$\begin{aligned} \text{Seq}(t) &\hat{=} (\text{SeqTm}(t), \star) \\ \text{SeqTm}(t \ s) &\hat{=} (\text{SeqTm}(t), \dashv, \text{SeqTm}(s), \textcircled{\@}) \\ \text{SeqTm}(\lambda.t) &\hat{=} (\text{Seq}(t), \lambda) \\ \text{SeqTm}(n) &\hat{=} (\text{SeqInd}(n), \boxminus) \\ \text{SeqInd}(0) &\hat{=} () \\ \text{SeqInd}(n + 1) &\hat{=} (\text{SeqInd}(n), \boxplus) \end{aligned}$$

We write  $C \xrightarrow{\text{Seq}(t)} C'$  for  $C \xrightarrow{f_1} \dots \xrightarrow{f_m} C'$  where  $\text{Seq}(t) = (f_1, \dots, f_m)$ .

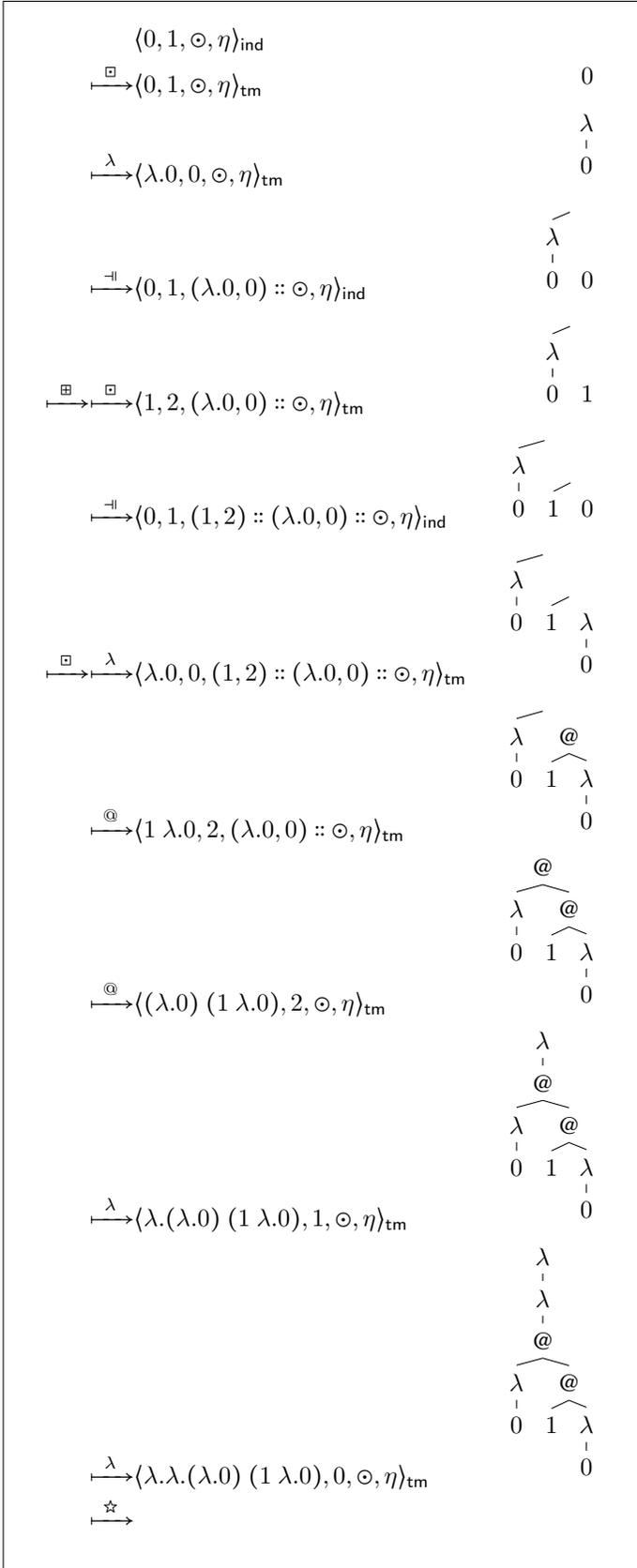
**Lemma 20.** If  $t'$  is closed, then  $\langle 0, 1, \odot, (t, e) \rangle_{\text{ind}} \xrightarrow{\text{Seq}(t')} \langle t, (t', \epsilon) :: e, [] \rangle_{\text{ev}}$ .

This lemma allows us to prove the correspondence between the AB machine and applicative bisimilarity.

**Theorem 21.**  $(t, e) \approx_{\text{app}} (s, d)$  iff  $\langle t, e, [] \rangle_{\text{ev}} \approx_m \langle s, d, [] \rangle_{\text{ev}}$ .

*Sketch.* To prove that machine bisimilarity implies applicative bisimilarity, we show that

$$\{((t, e), (s, d)) \mid \langle t, e, [] \rangle_{\text{ev}} \approx_m \langle s, d, [] \rangle_{\text{ev}}\}$$



**Fig. 5:** Example of argument generation

is an applicative bisimulation. Roughly,  $(t, e)$  evaluating to  $(\lambda.t', e')$  means that  $(s, d)$  evaluates to  $(\lambda.s', d')$  thanks to  $\odot$ . Then for all closed  $t''$ , the sequence  $\langle 0, 1, \odot, (t', e') \rangle_{\text{ind}} \xrightarrow{\text{Seq}(t'')} \langle t', (t'', \epsilon) :: e', [] \rangle_{\text{ev}}$  can only be matched by  $\langle 0, 1, \odot, (s', d') \rangle_{\text{ind}} \xrightarrow{\text{Seq}(t'')} \langle s', (t'', \epsilon) :: d', [] \rangle_{\text{ev}}$ , so we can conclude.

For the reverse implication, we show that

$$\begin{aligned} \mathcal{R} \triangleq & \{ \langle (t, e, [])_{\text{ev}}, (s, d, [])_{\text{ev}} \mid (t, e) \approx_{\text{app}} (s, d) \} \\ & \cup \{ \langle (n, \kappa, \rho, (t, e))_{\text{ind}}, (n, \kappa, \rho, (s, d))_{\text{ind}} \mid \\ & \quad (\lambda.t, e) \approx_{\text{app}} (\lambda.s, d) \} \\ & \cup \{ \langle (t', \kappa, \rho, (t, e))_{\text{tm}}, (t', \kappa, \rho, (s, d))_{\text{tm}} \mid \\ & \quad (\lambda.t, e) \approx_{\text{app}} (\lambda.s, d) \} \end{aligned}$$

is a machine bisimulation, which is easy to check.  $\square$

### C. Translation into HOcore

Figure 6 gives the translation of the AB machine in HOcore. We detail each component, starting with the evaluation mode, i.e., the KAM. We follow the same principles as in Section III: a non-empty stack  $\pi$  or environment  $e$  is represented by a pair of messages, respectively on  $hd_c$  and  $c$ , and  $hd_e$  and  $env$ . A closure is represented by two messages, one containing the term on  $\eta_1$  and one containing the environment on  $\eta_2$ . The process representing the empty environment  $\epsilon$  should never be executed, because all the closures we manipulate are closed; as a result, we can choose any process to represent it, e.g.,  $\mathbf{0}$ . The empty stack  $\llbracket [] \rrbracket$  and the process  $P_{\text{rec}}$  are used to generate an argument and are explained later.

The encoding of  $t s$  simulates the rule PUSH: we receive the current stack and environment  $e$  to create the new stack with  $(s, e)$  on top. Because we receive the current environment to put it on the stack, we have to recreate it on  $env$ , unchanged. In the encoding of  $\lambda.t$ , we capture the stack and environment, and if the stack is non-empty, we fetch its head  $\eta$  to create a new environment with  $\eta$  on top. Finally, a de Bruijn index  $n > 0$  go through the current environment, until we reach the correct closure (case  $n = 0$ ). In that case, we receive the head  $\eta$  and tail of the environment, discard the tail as it is no longer useful, and we restore the term and environment stored in  $\eta$ .

If  $\lambda.t$  is run in the environment  $e$  and the empty stack  $[]$ , then we obtain  $\llbracket [] \rrbracket \parallel hd_c(z).(\llbracket t \rrbracket \parallel \overline{env}(hd_e(z) \parallel \overline{env}(\llbracket e \rrbracket)))$ , so  $\llbracket [] \rrbracket$  has to start the argument generating process, and the result has then to be sent on  $hd_c$  for the evaluation to restart. We write  $\text{Stuck}((t, e))$  for the process in parallel with  $\llbracket [] \rrbracket$ , which remains stuck during the whole generation process. We now explain how  $\langle n, \kappa, \rho, \eta \rangle_{\text{ind}}$  and  $\langle t, \kappa, \rho, \eta \rangle_{\text{tm}}$  are encoded, starting with  $\kappa$  and  $\rho$ .

The machine distinguishes cases based on whether  $\kappa$  is 0 or not, to know if we should apply the transition LAMBDA or LAMBDA0. In the encoding of these rules (see the definition of Lambda), we send on name  $zero$  the expected behavior if  $\kappa = 0$ , and on  $succ$  what to do otherwise. The translation of the counter receives both messages, executes the corresponding

## 1) Evaluation mode

$$\begin{aligned} \llbracket (t, e, \pi)_{\text{ev}} \rrbracket &\triangleq \llbracket t \rrbracket \parallel \overline{\text{env}}\langle \llbracket e \rrbracket \rangle \parallel \overline{c}\langle \llbracket \pi \rrbracket \rangle \parallel P_{\text{rec}} & \llbracket (t, e) \rrbracket &\triangleq \overline{\eta_1}\langle \llbracket t \rrbracket \rangle \parallel \overline{\eta_2}\langle \llbracket e \rrbracket \rangle \\ \llbracket [] \rrbracket &\triangleq \odot(\_).(\overline{\text{ind}}\langle \llbracket 0 \rrbracket \rangle \parallel \overline{k}\langle \llbracket 1 \rrbracket_c \rangle \parallel \overline{r}\langle \llbracket \odot \rrbracket \rangle \parallel \overline{\text{initInd}}\langle \mathbf{0} \rangle) & \llbracket \epsilon \rrbracket &\triangleq \mathbf{0} \\ \llbracket \eta :: \pi \rrbracket &\triangleq \overline{\text{hd}}_c\langle \llbracket \eta \rrbracket \rangle \parallel \overline{c}\langle \llbracket \pi \rrbracket \rangle & \llbracket \eta :: e \rrbracket &\triangleq \overline{\text{hd}}_e\langle \llbracket \eta \rrbracket \rangle \parallel \overline{\text{env}}\langle \llbracket e \rrbracket \rangle \end{aligned}$$

$$\begin{aligned} \llbracket t s \rrbracket &\triangleq \text{App}_{\text{eval}}(\llbracket t \rrbracket, \llbracket s \rrbracket) & \text{App}_{\text{eval}}(P_t, P_s) &\triangleq c(x).\text{env}(y).(P_t \parallel \overline{c}\langle \overline{\text{hd}}_c\langle \overline{\eta_1}\langle P_s \rangle \parallel \overline{\eta_2}\langle y \rangle \rangle \parallel x \rangle \parallel \overline{\text{env}}\langle y \rangle) \\ \llbracket \lambda.t \rrbracket &\triangleq \text{Lam}_{\text{eval}}(\llbracket t \rrbracket) & \text{Lam}_{\text{eval}}(P_t) &\triangleq c(x).(x \parallel \text{hd}_c(y).\text{env}(z).(P_t \parallel \overline{\text{env}}\langle \overline{\text{hd}}_e\langle y \rangle \parallel \overline{\text{env}}\langle z \rangle \rangle)) \\ \llbracket n + 1 \rrbracket &\triangleq \text{Ind}_{\text{eval}}(\llbracket n \rrbracket) & \text{Ind}_{\text{eval}}(P_n) &\triangleq \text{env}(x).(x \parallel \text{hd}_e(\_).P_n) \\ \llbracket 0 \rrbracket &\triangleq \text{env}(x).(x \parallel \text{hd}_e(y).\text{env}(\_).(y \parallel \eta_1(y_1).\eta_2(y_2).(y_1 \parallel \overline{\text{env}}\langle y_2 \rangle))) & & \\ P_{\text{rec}} &\triangleq \text{Reclnd} \parallel \overline{\text{recind}}\langle \text{Reclnd} \rangle \parallel \text{RecTm} \parallel \overline{\text{rectm}}\langle \text{RecTm} \rangle \parallel \text{RecMax} \parallel \overline{\text{recmax}}\langle \text{RecMax} \rangle \end{aligned}$$

2) Counter  $\kappa$ , stack  $\rho$ , and stuck process  $\text{Stuck}(\eta)$ 

$$\begin{aligned} \llbracket 0 \rrbracket_c &\triangleq \text{zero}(x).\text{succ}(\_).x & \llbracket \odot \rrbracket &\triangleq \text{mt}(x).\text{cons}(\_).x \\ \llbracket \kappa + 1 \rrbracket_c &\triangleq \text{Suk}(\llbracket \kappa \rrbracket_c) & \llbracket (t, \kappa) :: \rho \rrbracket &\triangleq \text{ConsR}(\llbracket (t, \kappa) \rrbracket, \llbracket \rho \rrbracket) \\ \text{Suk}(P_\kappa) &\triangleq \overline{\text{suk}}\langle P_\kappa \rangle \parallel \text{zero}(\_).\text{succ}(x).x & \text{ConsR}(P_{hd}, P_\rho) &\triangleq \overline{\text{hd}}_r\langle P_{hd} \rangle \parallel \overline{r}\langle P_\rho \rangle \parallel \text{mt}(\_).\text{cons}(x).x \\ \text{Stuck}((t, e)) &\triangleq \text{hd}_c(z).(\llbracket t \rrbracket \parallel \overline{\text{env}}\langle \overline{\text{hd}}_e\langle z \rangle \parallel \overline{\text{env}}\langle \llbracket e \rrbracket \rangle \rangle) & \llbracket (t, \kappa) \rrbracket &\triangleq \overline{w_1}\langle \llbracket t \rrbracket \rangle \parallel \overline{w_2}\langle \llbracket \kappa \rrbracket_c \rangle \end{aligned}$$

## 3) Creation of indices

$$\begin{aligned} \llbracket \langle n, \kappa, \rho, \eta \rangle_{\text{ind}} \rrbracket &\triangleq \overline{\text{ind}}\langle \llbracket n \rrbracket \rangle \parallel \overline{k}\langle \llbracket \kappa \rrbracket_c \rangle \parallel \overline{r}\langle \llbracket \rho \rrbracket \rangle \parallel P_{\text{rec}} \parallel \overline{\text{initInd}}\langle \mathbf{0} \rangle \parallel \text{Stuck}(\eta) \\ \text{Reclnd} &\triangleq \overline{\text{initInd}}(\_).\text{recind}(x).(x \parallel \overline{\text{recind}}\langle x \rangle \parallel \text{Succ} + \text{Var}) \\ \text{Succ} &\triangleq \boxplus(\_).\text{ind}(x).k(y).(\overline{\text{ind}}\langle \text{Ind}_{\text{eval}}(x) \rangle \parallel \overline{k}\langle \text{Suk}(y) \rangle \parallel \overline{\text{initInd}}\langle \mathbf{0} \rangle) \\ \text{Var} &\triangleq \boxminus(\_).\text{ind}(x).(\overline{\text{tm}}\langle x \rangle \parallel \overline{\text{initTm}}\langle \mathbf{0} \rangle) \end{aligned}$$

## 4) Creation of terms

$$\begin{aligned} \llbracket (t, \kappa, \rho, \eta)_{\text{tm}} \rrbracket &\triangleq \overline{\text{tm}}\langle \llbracket t \rrbracket \rangle \parallel \overline{k}\langle \llbracket \kappa \rrbracket_c \rangle \parallel \overline{r}\langle \llbracket \rho \rrbracket \rangle \parallel P_{\text{rec}} \parallel \overline{\text{initTm}}\langle \mathbf{0} \rangle \parallel \text{Stuck}(\eta) \\ \text{Lambda}(P_\kappa) &\triangleq \lambda(\_).\text{tm}(x). \left( \overline{\text{tm}}\langle \text{Lam}_{\text{eval}}(x) \rangle \parallel P_\kappa \parallel \overline{\text{zero}}\langle \overline{k}\langle P_\kappa \rangle \parallel \overline{\text{initTm}}\langle \mathbf{0} \rangle \rangle \right. \\ &\quad \left. \parallel \overline{\text{succ}}\langle \text{suk}(y).(\overline{k}\langle y \rangle \parallel \overline{\text{initTm}}\langle \mathbf{0} \rangle) \rangle \right) \\ \text{AppFun}(P_\kappa, P_\rho) &\triangleq \text{=}\!|\!|(\_).\text{tm}(x).(\overline{r}\langle \text{ConsR}(\overline{\text{hd}}_r\langle \overline{w_1}\langle x \rangle \parallel \overline{w_2}\langle P_\kappa \rangle \rangle, P_\rho) \rangle \parallel \overline{\text{ind}}\langle \llbracket 0 \rrbracket \rangle \parallel \overline{k}\langle \llbracket 1 \rrbracket_c \rangle \parallel \overline{\text{initInd}}\langle \mathbf{0} \rangle) \\ \text{Done} &\triangleq \star(\_).\text{tm}(x).(\overline{\text{hd}}_c\langle \overline{\eta_1}\langle x \rangle \parallel \overline{\eta_2}\langle \llbracket \epsilon \rrbracket \rangle \rangle \parallel \overline{c}\langle \llbracket [] \rrbracket \rangle) \\ \text{App}(P_\kappa, P_{hd}, P_\rho) &\triangleq \text{@}(\_).\text{tm}(x_2). \left( P_{hd} \parallel \overline{w_2}\langle y \rangle.\overline{w_1}\langle x_1 \rangle. \left( \overline{\text{max1}}\langle y \rangle \parallel \overline{\text{max2}}\langle P_\kappa \rangle \parallel \overline{\text{init1}}\langle y \rangle \parallel \overline{\text{init2}}\langle P_\kappa \rangle \parallel \right. \right. \\ &\quad \left. \left. \overline{\text{resu}}\langle z \rangle.(\overline{\text{tm}}\langle \text{App}_{\text{eval}}(x_1, x_2) \rangle \parallel P_\rho \parallel \overline{k}\langle z \rangle \parallel \overline{\text{initTm}}\langle \mathbf{0} \rangle) \right) \right) \\ \text{RecMax} &\triangleq \overline{\text{init1}}\langle x_1 \rangle.\overline{\text{init2}}\langle x_2 \rangle.\text{recmax}(y).(y \parallel \overline{\text{recmax}}\langle y \rangle \parallel \text{Max}(x_1, x_2)) \\ \text{Max}(P_1, P_2) &\triangleq P_1 \parallel \overline{\text{zero}}\langle \overline{\text{max2}}\langle x \rangle.\overline{\text{resu}}\langle x \rangle \rangle \parallel \overline{\text{succ}}\left\langle \text{suk}(x_1). \left( P_2 \parallel \overline{\text{zero}}\langle \overline{\text{max1}}\langle x \rangle.\overline{\text{resu}}\langle x \rangle \rangle \right. \right. \\ &\quad \left. \left. \parallel \overline{\text{succ}}\langle \text{suk}(x_2).(\overline{\text{init1}}\langle x_1 \rangle \parallel \overline{\text{init2}}\langle x_2 \rangle) \rangle \right) \right\rangle \\ \text{RecTm} &\triangleq \overline{\text{initTm}}(\_).\text{rectm}(x). \left( \begin{array}{l} x \parallel \overline{\text{rectm}}\langle x \rangle \parallel r(y).y \parallel \overline{\text{cons}}\left\langle k(z).\text{hd}_r(y_1).r(y_2). \right. \\ \quad \left. (\text{Lambda}(z) \parallel y) + \text{AppFun}(z, y) + \text{App}(z, y_1, y_2) \right\rangle \\ \parallel \overline{\text{mt}}\left\langle k(z). \left( \begin{array}{l} z \parallel \overline{\text{zero}}\left\langle \begin{array}{l} (\text{Lambda}(z) \parallel \llbracket \odot \rrbracket \rangle) \\ + \text{AppFun}(z, \llbracket \odot \rrbracket \rangle) + \text{Done} \end{array} \right\rangle \right. \\ \quad \left. \parallel \overline{\text{succ}}\left\langle \text{suk}(\_). \begin{array}{l} (\text{Lambda}(z) \parallel \llbracket \odot \rrbracket \rangle) \\ + \text{AppFun}(z, \llbracket \odot \rrbracket \rangle) \end{array} \right\rangle \right) \right\rangle \end{array} \right) \end{array} \right)$$

Fig. 6: Translation of the AB machine

one (e.g., the one on *zero* for the encoding of 0), and discards the other. Apart from that,  $\kappa$  is translated as a natural number. Similarly, the translation of  $\rho$  combines the regular encodings of pairs and stacks, but also indicates whether  $\rho$  is empty or not, to know if we can apply the transitions APP and RESTART.

After flagging  $\odot$ , the process  $\llbracket [] \rrbracket$  starts the argument generation process in index mode: the index being built is sent on *ind* (here, initialized with  $\llbracket 0 \rrbracket$ ), the counter on *k*, and the stack on *r*. The message on *initInd* triggers the recursive process *ReInd*, which non-deterministically chooses between Succ and Var. Executing Succ flags  $\boxplus$ , increases the values of the index (thanks to  $\text{Ind}_{\text{eval}}$ ) and the counter (with *Suk*), and relaunches the *ReInd* process with a message on *initInt*. Executing Var flags  $\boxminus$ , moves the index from *ind* to *tm*, and initiates the term mode by sending a message on *initTm*, which triggers the process *RecTm*.

The goal of *RecTm* is to non-deterministically choose between the four transitions available in term mode, namely  $\xrightarrow{\lambda}$ ,  $\xrightarrow{-\parallel}$ ,  $\xrightarrow{\textcircled{a}}$ , and  $\xrightarrow{\star}$ . However, some of these transitions have requirements:  $\xrightarrow{\textcircled{a}}$  needs  $\rho \neq \odot$  and  $\xrightarrow{\star}$  needs  $\rho = \odot$  and  $\kappa = 0$ . The process *RecTm* is therefore doing a case analysis to check these conditions. First, it captures  $\llbracket \rho \rrbracket$  on *r*: if  $\rho \neq \odot$ , it executes the message on *cons*, which makes a choice between  $\lambda$ ,  $-\parallel$ , and  $\textcircled{a}$ , which are represented by respectively Lambda, AppFun, and App. If  $\rho = \odot$ , then we do a case analysis on  $\kappa$ . If  $\kappa = 0$ , then we can do either  $\lambda$ ,  $-\parallel$ , or  $\star$  (represented by Done), otherwise, only  $\lambda$  or  $-\parallel$  are possible.

The process Lambda adds a  $\lambda$ -abstraction to the term in *tm*, updating  $\kappa$  (represented by  $P_\kappa$ ) accordingly: if  $\kappa = 0$ , then it is restored unchanged on *k*, otherwise, it is decreased by 1 by releasing the message in *suk*. The process AppFun pushes on the stack  $P_\rho$  the current term  $t_1$  on *tm* and its counter  $\kappa_1$  (represented by  $P_\kappa$ ), which is the term in function position of an application. It then relaunches the index mode to build the argument  $t_2$  with its counter  $\kappa_2$ . The process App can then build the application itself, by computing the maximum between  $\kappa_1$  and  $\kappa_2$  with the processes *RecMax* and *Max*.

We compute the maximum between  $\kappa_1$  and  $\kappa_2$  by removing the layers of successors common to  $\kappa_1$  and  $\kappa_2$ , until we reach 0 for one of them. If we reach 0 for  $\kappa_1$  first, then  $\kappa_2$  is the max, otherwise it is  $\kappa_1$ . We store the initial values of  $\kappa_1$  and  $\kappa_2$  in respectively *max1* and *max2*, and the decomposition occurs in *Max*, where *init1* is initialized with  $\kappa_1$  and *init2* with  $\kappa_2$ . If  $P_1 = \llbracket \kappa_1 \rrbracket_c = \llbracket 0 \rrbracket_c$ , then we send  $\kappa_2$  (stored in *max2*) on *resu*. Otherwise,  $P_1 = \text{suk}\langle P'_1 \rangle$ , and we do a case analysis on the process  $P_2 = \llbracket \kappa_2 \rrbracket_c$ . If  $P_2 = \llbracket 0 \rrbracket_c$ , then we send  $\kappa_1$  on *resu*, otherwise  $P_2 = \text{suk}\langle P'_2 \rangle$ , and we restart *RecMax* by sending  $P'_1$  and  $P'_2$  on *init1* and *init2*, respectively. Once the max is known on *resu*, then App builds the application  $t_1 t_2$  and relaunches *RecTm*.

Finally, the process Done ends the argument generation phase, and restarts the computation by restoring the empty continuation and by passing the term in *tm* to *Stuck*( $\eta$ ). The process  $P_{\text{rec}}$  contains all the processes necessary to encode the different recursive mechanisms.

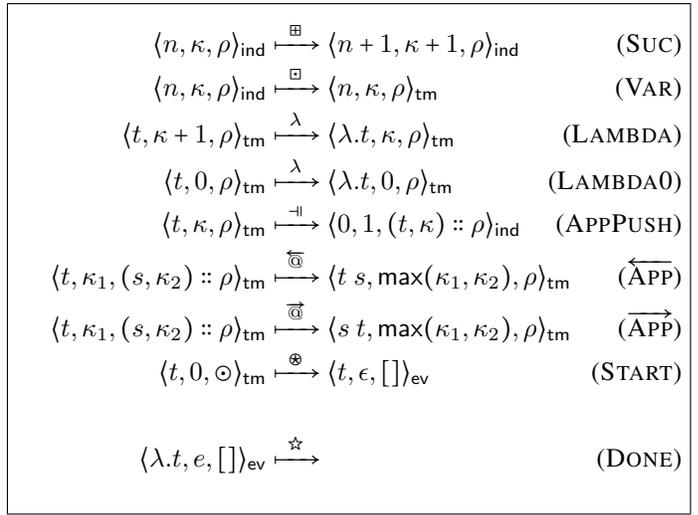


Fig. 7: Contextual equivalence machine

*Full abstraction:* We use the notions of Section IV-D to prove the correspondence between the AB machine and its translation. The definition of next carries over to the translation of the AB machine, and Lemmas 12 and 13 still hold (but without terminating transitions). Recall that  $\approx_{\text{HO}}$  stands for  $\approx_{\text{HO}}^{\mathbb{H}}$ , where  $\mathbb{H}$  contains the names of the translation that are not flags. The proof of the following theorem is then the same as for Theorem 14.

**Theorem 22.**  $C \approx_m C'$  iff  $\llbracket C \rrbracket \approx_{\text{HO}} \llbracket C' \rrbracket$ .

We then deduce a full abstraction result between  $\lambda$ -calculus with applicative bisimilarity and HOcore.

**Corollary 23.** If  $(t, e)$  and  $(s, d)$  are closed closures, then  $(t, e) \approx_{\text{app}} (s, d)$  iff  $\llbracket \langle t, e, [] \rangle_{\text{ev}} \rrbracket \approx_{\text{HO}} \llbracket \langle s, d, [] \rangle_{\text{ev}} \rrbracket$ .

#### D. Internalizing Contextual Equivalence

Corollary 23 is enough to deduce full abstraction w.r.t. contextual equivalence, since  $t \approx_{\text{ctx}} s \iff (t, \epsilon) \approx_{\text{app}} (s, \epsilon)$ . However, it is possible to prove this result directly, by internalizing contextual equivalence in an abstract machine.

Figure 7 gives the transitions of this machine, except for the  $\xrightarrow{\tau}$  transitions, which are the same as in Section V-A. In contrast with the AB machine, the contextual equivalence machine produces a context first, and then reduces the resulting term; consequently, the starting point is a state  $\langle t, 0, \odot \rangle_{\text{tm}}$ , where  $t$  is the closed term we want to plug in the context. When the context is finished, the transition  $\xrightarrow{\textcircled{\star}}$  switches to the evaluation mode. Also, the evaluation part of the machine is not executed several times, since  $\approx_{\text{ctx}}$  is not coinductive. We flag  $\star$  when the evaluation terminates, to distinguish a terminating term from a diverging one.

Creating a context  $\mathcal{C}$  is almost the same as generating an argument in the AB machine, except that we want to plug a closed term  $t$  inside. We build  $\mathcal{C}[t]$  by starting the generation process from  $t$ ;  $t$  can be anywhere in  $\mathcal{C}[t]$ , not necessarily at the leftmost position, so we cannot do the generation process going left to right in an application, as with the AB machine (Section V-B). Instead, after producing a term  $t$  and with a

term  $s$  on the stack, we can do either the transition `APPLEFT` to build  $t s$ , or `APPRIGHT` to build  $s t$ .

**Example 24.** We show how to generate the context  $\lambda.(0\ 0) (\square\ 0)$  around  $t$ .

$$\begin{aligned}
\langle t, 0, \odot \rangle_{\text{tm}} &\xrightarrow{-i} \xrightarrow{\square} \langle 0, 1, (t, 0) :: \odot \rangle_{\text{tm}} \\
&\xrightarrow{\overline{\odot}} \langle t, 0, 1, \odot \rangle_{\text{tm}} \\
&\xrightarrow{-i} \xrightarrow{\square} \langle 0, 1, (t, 0, 1) :: \odot \rangle_{\text{tm}} \\
&\xrightarrow{-i} \xrightarrow{\square} \langle 0, 1, (0, 1) :: (t, 0, 1) :: \odot \rangle_{\text{tm}} \\
&\xrightarrow{\overline{\odot}} \langle 0, 0, 1, (t, 0, 1) :: \odot \rangle_{\text{tm}} \\
&\xrightarrow{\overline{\odot}} \langle (0\ 0) (t, 0), 1, \odot \rangle_{\text{tm}} \\
&\xrightarrow{\lambda} \langle \lambda.(0\ 0) (t, 0), 0, \odot \rangle_{\text{tm}}
\end{aligned}$$

The translation of the contextual equivalence machine into `HOcore` and the full abstraction proofs are similar to the `AB` machine ones.

**Theorem 25.** If  $t$  and  $s$  are closed terms, then  $t \approx_{\text{ctx}} s$  iff  $\llbracket \langle t, 0, \odot \rangle_{\text{tm}} \rrbracket \approx_{\text{HO}} \llbracket \langle s, 0, \odot \rangle_{\text{tm}} \rrbracket$ .

## VI. CONCLUSION AND FUTURE WORK

We propose encodings of the call-by-name  $\lambda$ -calculus into `HOcore`, fully abstract w.r.t. normal-form and applicative bisimilarities, and contextual equivalence. This shows that a minimal higher-order calculus with a fixed number of hidden names, which is much less expressive than the name-passing  $\pi$ -calculus, still has enough expressive power to faithfully encode the call-by-name  $\lambda$ -calculus.

Because we rely on abstract machines, our encodings are not compositional. We use abstract machines not only to fix the reduction strategy, but also as an intermediary step between the  $\lambda$ -calculus and `HOcore`. We turn the equivalences of the  $\lambda$ -calculus, and their potentially complex testing conditions, into a first-order bisimilarity over a LTS (a flags-generating machine), which is closer to the `HOcore` equivalence. We believe this internalization technique can be applied to other languages for which an abstract machine has been defined, like, e.g., the call-by-value calculus and its CK machine [12] (see [8, Appendix C]), or a calculus with control operators [7]. Even though the bisimilarities for these calculi can be quite intricate, it should be always possible to generate a context as in Section V-D to internalize contextual equivalence.

The encodings of the extended abstract machines into `HOcore` rely on the same principles, e.g., to represent stacks, non-deterministic choice, case analyses on terms, etc. We believe it is possible to automatically derive the encoding from an abstract machine, so that the generated translation is deterministic (up to flags for choice processes, as in Lemma 12) and with an operational correspondence result similar to Lemma 13. As these two ingredients are almost sufficient to get full abstraction between machines and `HOcore`, it would give us Theorem 14 or Theorem 22 for free.

Finally, we aim at translating the full  $\lambda$ -calculus, without fixing a reduction strategy. Such an encoding is proposed in [9], where a  $\lambda$ -term is represented as a tree, and  $\beta$ -reduction

is a transformation on trees. It relies on an unbounded number of restricted names to represent tree nodes; we wonder if we can use the same ideas with only a fixed number of names.

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